

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Confirmation No. 1814

Group No.: 2666

Examiner: R.C. Scheibel, Jr. 09/745,655

Docket No.: AMCC4840 12/06/2005 Issued:

SYSTEM AND METHOD FOR PROGRAMMING THE VALUE OF

FRAME SYNCHRONIZATION WORDS IN A MULTIDIMENSIONAL

DIGITAL FRAME STRUCTURE

CERTIFICATION UNDER 37 CFR § 1.8

I hereby certify that the documents referred to as enclosed herein are being deposited with the United States Postal Service as first class mail on this date (Combo 13 100 in an envelope addressed to: Commissioner for Patents, Box 1450, Alexandria, VA. 22313-1450

Signature Date

6,973,099 131

MAIL STOP: REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents

P. O. Box 1450

Serial No.:

For:

Alexandria, VA. 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION

The Assignee, APPLIED MICRO CIRCUITS CORPORATION, of the referenced patent hereby requests that a Certificate of Correction be issued to add References Cited by the Examiner in an Office Action dated 06/15/2004.

Respectfully submitted,

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Add the following to the title page

References Cited

U.S. PATENT DOCUMENTS

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